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LTYSON
Attorney Docket no: JPL 9653

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shunpei Yamazaki et al.

Serial No.: 09/522,428

Filed: March 9, 2000

For: Display Device

Examiner: L. Jorgensen

Art Unit: 2675

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: the Assistant Commissioner for Patents, Washington, D.C. 20231 on December 30, 2002
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Rachelle Hammerquist
Name of applicant, assignee, or Registered Rep.
Rachelle Hammerquist 12-30-02
Signature Date

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AMENDMENT C

Commissioner for Patents
Washington D.C. 20231

In response to the Office Action dated August 28, 2002, a one month extension of time being

separately submitted, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend the claims as follows:

Sub B1
C1
1.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix; and

a source driver and a gate driver which drive said active matrix circuit,

wherein n bit information out of m bit digital video data inputted from an external is used for a voltage gray scale method, and (m-n) bit information is used for a time ratio gray scale method, where said m and said n are integers equal to or larger than 2 and satisfy m>n, and